<u>REMARKS</u>

Reconsideration of this application as amended is respectfully requested.

Claim 25 has been amended. No claims have been cancelled or added.

Claims 1, 4-5, 7, 14, 18-20, 25, 29-30, 35, 37-39, 41, 44-45, 50-52, and 54 are rejected under 35 U.S.C. 102(b) as being unpatentable over U.S. Patent No. of 5, 436,655 of Kaewell, Jr. et al (Kaewell). The Examiner states that

> Regarding claims 1, 25, 41 and 54, Kaewell teaches a method for supporting DSP of a plurality of data types comprising the steps of continuing broadcasting a plurality of firmware algorithm to a plurality of DSP engines over a channelized serial bus (T1), selectively monitoring and receiving at least one firmware algorithm of the plurality of algorithm by at least one DSP engine wherein the at least one firmware algorithm is used to process data of at least one corresponding data type received by the at least one DSP engine over at least one data line ().

(p. 2, Office Action 08/15/01).

Applicants respectfully submit that Kaewell does not anticipate Claims 1, 25, 41, and 54. Claims 1, 25, 41, and 54 state that

> *continuously broadcasting* a plurality of firmware algorithms to a plurality of DSP engines over a channelized serial bus;

(Claim 1) (Emphasis added).

> a serial bus comprising at least one channel over which a plurality of firmware algorithms are *continuously broadcasted*;

(Claim 25) (Emphasis added).

> a serial bus coupled to the master DSP engine, the serial bus comprising a plurality of channels over which a plurality of firmware algorithms are *continuously broadcasted*;

(Claim 41) (Emphasis added).

> *continuously broadcasting* a plurality of firmware algorithms to a plurality of DSP engines over a channelized serial bus;

(Claim 54) (Emphasis added).

In contrast, Kaewell does not disclose continuously broadcasting a plurality of firmware algorithms to a plurality of DSP engines over a channelized serial bus. Kaewell states that

> More specifically, the firmware is downloaded via the SPDF interface 32, arbiter logic 31, bus 30, and DPRAMs 26, 27, 28, and 29 to the SRAMs 41, 42, 43, and 44 and via the SPDF interface 32 and arbiter logic 31 directly to the SRAM 45. This firmware can later be replaced by firmware that reconfigures the system to operate in a purely digital fashion.

(Kaewell, Col. 4, lines 14-21). In other words, Kaewell uses the T1 line to download firmware. Therefore, Kaewell does not anticipate claims 1, 25, 41, and 54 for at least these reasons.

Given that claims 2-24 depend from claim 1, claims 26-40 depend from claim 25, claims 42-53 depend from claim 41, and claims 55-62 depend from claim 54, applicants submit that claims 1, 4-5, 7, 14, 18-20, 25, 29-30, 35, 37-39, 41, 44-45, 50-52, and 54 are not anticipated by Kaewell under 35 U.S.C. §102(b) by the references cited by the Examiner for at least these reasons.

Claims 6, 15-17, 21, 26, 36, 49, 57, 61 are rejected under 35 U.S.C. 103(a) as being unpatentable over Kaewell for obviousness. Kaewell neither teaches nor suggests the above-described limitation of continuously broadcasting a plurality of firmware algorithms to a plurality of DSP engines over a channelized serial bus. Therefore, given that claims 2-24 depend from claim 1, claims 26-40 depend from claim 25, claims 42-53 depend from claim 41, and claims 55-62 depend from claim 54, applicants submit that claims 6, 15-17, 21, 26, 36, 49, 57, 61 are not rendered obvious under 35 U.S.C. §103(a) by Kaewell cited for at least these reasons.

Claims 2-3, 8-13, 22-24, 27-28, 31-34, 40, 42-43, 46-48, 53, 55-56, and 58-60 were objected to by the Examiner, and should now be made allowable.

If the Examiner believes a telephone conference would expedite or assist in the allowance of the present application, the Examiner is invited to call Stephen Neal at (408) 720-8300.

If any fee is due not covered by any check submitted please charge Deposit Account No. 02-2666.

Respectfully submitted,

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25.   (Amended)   An apparatus for supporting digital signal processing (DSP) of a plurality of data types, the apparatus comprising:

a serial bus comprising at least one channel over which a plurality of firmware algorithms are <u>continuously</u> broadcasted; and

a plurality of DSP engines coupled to the serial bus and to at least one data line, at least one of the plurality of DSP engines selectively monitoring for and receiving at least one firmware algorithm of the plurality of firmware algorithms broadcasted, wherein the at least one firmware algorithm is used to process data received by the at least one of the plurality of DSP engines over the at least one data line.